



## Course Specifications

<b>Course Title:</b>	Computer Architecture
<b>Course Code:</b>	CSI 323
<b>Program:</b>	Computer Science and Information
<b>Department:</b>	Computer Science and Information
<b>College:</b>	College of Science at Az Zulfi
<b>Institution:</b>	Majmaah University

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## A. Course Identification

<b>1. Credit hours:</b> (3) (2 Lec + 2 lab)
<b>2. Course type</b>
a. University <input type="checkbox"/> College <input type="checkbox"/> Department <input checked="" type="checkbox"/> Others <input type="checkbox"/>
b. Required <input checked="" type="checkbox"/> Elective <input type="checkbox"/>
<b>3. Level/year at which this course is offered:</b> 6 <sup>th</sup> Level – 3 <sup>rd</sup> year
<b>4. Pre-requisites for this course (if any):</b> Computer Organization and Assembly Language – CS1 313
<b>5. Co-requisites for this course (if any):</b> N/A

### 6. Mode of Instruction (mark all that apply)

No	Mode of Instruction	Contact Hours	Percentage
1	Traditional classroom	48	80%
2	Blended	6	10%
3	E-learning	0	0%
4	Distance learning	0	0%
5	Other	6	10%

### 7. Contact Hours (based on academic semester)

No	Activity	Contact Hours
1	Lecture	30
2	Laboratory/Studio	15
3	Tutorial	15
4	Others (specify)	-
	<b>Total</b>	60

## B. Course Objectives and Learning Outcomes

<p><b>1. Course Description</b></p> <p>This course provides the study of the structure, characteristics and operation of modern computer systems. It will focus on basic processor organization, Instruction Set Representation, ALU Design and Implementation, Processor Design Implementation, Memory Design and Organization, Input–Output Design and Organization, and Introduction to Multiprocessors.</p>
<p><b>2. Course Main Objective</b></p> <ul style="list-style-type: none"> <li>To understand the fundamentals concepts of computer organization and architecture.</li> <li>To provides the basic structure , characteristics and operations of modern computer systems.</li> <li>To understand the basic processor organization, data and control paths, instruction-level parallelism, pipelining, and multiple issue.</li> </ul>

- To understand memory design and implementation of modern computer system, storage and input-output devices and their interfacing to the processor and memory.
- To understand the basics of storage and input-output devices and their interfacing to the processor and memory.

### 3. Course Learning Outcomes

CLOs		Aligned PLOs
<b>1</b>	<b>Knowledge and Understanding</b>	
1.1	Students will learn about the concepts of computer organization and architecture.	k2
1.2	Students will be able to define the behavior and basic structure , characteristics and operations of modern computer systems.	k2
1.3		
1...		
<b>2</b>	<b>Skills :</b>	
2.1	Student will develop and apply knowledge of computer organization and architecture to design the current and modern world computers.	s1
2.2	Student will gain experience in the design, development, and performance analysis of modern world computers.	s1
2.3	Student will gain experience in the application of fundamental Computer Science methods and algorithms in the development of modern memory organization, storage devices and microprocessor for modern world computers .	s3
2...		
<b>3</b>	<b>Values:</b>	
3.1	Present a short report in a written form and orally using appropriate scientific language.	c3
3.2		
3.3		
3...		

### C. Course Content

No	List of Topics	Contact Hours
1	Introduction (Computer Abstractions and Technology)	4
2	Assessing and Understanding Performance	4
3	Instruction Set Architecture	8
4	Assemblers, Linkers, and the SPIM Simulator	8
5	Arithmetic for Computers (ALU)	8
6	Processor Design	12
7	Memory Hierarchy and Operation	8
8	Multiprocessor Systems	4
...		
<b>Total</b>		<b>56</b>



## D. Teaching and Assessment

### 1. Alignment of Course Learning Outcomes with Teaching Strategies and Assessment Methods

Code	Course Learning Outcomes	Teaching Strategies	Assessment Methods
<b>1.0</b>	<b>Knowledge and Understanding</b>		
1.1	Students will learn about the concepts of computer organization and architecture.	Lectures Lab demonstrations	Written Exam Homework assignments
1.2	Students will be able to define the behavior and basic structure, characteristics and operations of modern computer systems.	Case studies Individual presentations	Class & Lab Activities Quizzes
...			
<b>2.0</b>	<b>Skills</b>		
2.1	Student will develop and apply knowledge of computer organization and architecture to design the current and modern world computers.		
2.2	Student will gain experience in the design, development, and performance analysis of modern world computers.	Lectures Lab demonstrations Case studies Individual presentations	Written Exam assignments Lab Activities Quizzes
2.3	Student will gain experience in the application of fundamental Computer Science methods and algorithms in the development of modern memory organization, storage devices and microprocessor for modern world computers.	Brainstorming	
<b>3.0</b>	<b>Values</b>		
3.1	Present a short report in a written form and orally using appropriate scientific language.	Small group discussion	Written Exam
3.2	Learn how to search for information through library and internet.	Whole group discussion Brainstorming	Homework assignments Lab assignments
3.3	Communicate with teacher, ask questions, solve problems, and use computers.	Presentation	Class Activities Quizzes

### 2. Assessment Tasks for Students

#	Assessment task*	Week Due	Percentage of Total Assessment Score
1	First written mid-term exam	6	15%
2	Second written mid-term exam	12	15%
3	Class activities, group discussions, Presentation	Every 2 weeks	10%
4	Homework + Assignments	After Every chapter	10%
5	Electronic exam	14	10%

#	Assessment task*	Week Due	Percentage of Total Assessment Score
6	Lab activities	15	40%
7	Final written exam	16	15%
8			

\*Assessment task (i.e., written test, oral test, oral presentation, group project, essay, etc.)

## E. Student Academic Counseling and Support

### Arrangements for availability of faculty and teaching staff for individual student consultations and academic advice :

Arrangements for availability of faculty and teaching staff for individual student consultations and academic advice. (include amount of time teaching staff are expected to be available each week)

- 6-office hours per week in the lecturer schedule.
- The contact with students by e-mail, mobile, office telephone, website and BlackBoard.

## F. Learning Resources and Facilities

### 1. Learning Resources

<b>Required Textbooks</b>	David A. Patterson, John L. Hennessy, “computer organization and design”, 5 th edition Publishers, Inc., 2013
<b>Essential References Materials</b>	<ul style="list-style-type: none"> <li>• William Stallings, Computer Organization and Architecture: Designing for Performance, 10th Edition, Prentice Hall, 2015.</li> <li>• M. Morris Mano, Computer System Architecture, 3rd Custom Edition, Pearson.</li> </ul>
<b>Electronic Materials</b>	<a href="https://www.coursera.org/">https://www.coursera.org/</a>
<b>Other Learning Materials</b>	Videos and presentations made available on BlackBoard e-Learning platform.

### 2. Facilities Required

Item	Resources
<b>Accommodation</b> (Classrooms, laboratories, demonstration rooms/labs, etc.)	Classrooms with required digital aids and to support traditional method of teaching using blackboard. Classrooms with proper lighting and air conditioning system integrated with the sound System /audio system. Classroom with smart board interface, display screen and a computer to aid the sessions
<b>Technology Resources</b> (AV, data show, Smart Board, software, etc.)	Smart Board with supporting software / computers with updated versions of software as required to understand the subject concepts.
<b>Other Resources</b> (Specify, e.g. if specific laboratory equipment is required, list requirements or attach a list)	N/A

## G. Course Quality Evaluation

Evaluation Areas/Issues	Evaluators	Evaluation Methods
Strategies for Obtaining Student Feedback on Effectiveness of Teaching	Instructor	Analysis of students' results. Observation during class work. Students' evaluations. Colleagues' evaluations. Evaluation questionnaire filled by the students. Interview a sample of students enrolled in the course to solicit their opinions
Other Strategies for Evaluation of Teaching	the Department	Self-assessment. External evaluation. Periodic review of course (the Commission of study plans).
Processes for Improvement of Teaching	the Department	Taking into account the recommendations yielded from the internal review of the course. Guidelines about teaching the course provided by the study plans commission. Department guidelines pertaining the faculty member's performance acquired using direct observation. Training and development. Workshops to improve the educational process
Processes for Verifying Standards of Student Achievement	Instructor	check marking by an independent member teaching staff of a sample of student work, periodic exchange and remarking of tests or a sample of assignments with staff at another institution. Instructors of the course working together with Head of Department to adopt a unique process of the evaluation.
Describe the planning arrangements for periodically reviewing course effectiveness and planning for improvement.	Instructor	Comparison of the course to its counterparts offered in similar departments. Periodic revision of course description by faculty member. Periodic revision of course description by the study plans and schedules Commission. Update learning resources related to the course to ensure that the course is up-to-date with the developments in the field. Make use of statistical analysis of course evaluation carried out by the students to improve and develop the course. Provide an opportunity to the students to express their opinions about what is taught

Evaluation Areas/Issues	Evaluators	Evaluation Methods
		and receive suggestions and evaluate their effectiveness.

**Evaluation areas** (e.g., Effectiveness of teaching and assessment, Extent of achievement of course learning outcomes, Quality of learning resources, etc.)

**Evaluators** (Students, Faculty, Program Leaders, Peer Reviewer, Others (specify)

**Assessment Methods** (Direct, Indirect)



## H. Specification Approval Data

Council / Committee	
Reference No.	
Date	